Connor-Winfield's Timing IC's: Sync, Frequency Translation, Jitter

TIMING IC's : Sync, Frequency Translation, Jitter Attenuation and Multi Output Clock Generation	Model Number	Data Sheet PDFs	# Reference Inputs	Input Frequencies	# Analog PLL Chains	Total # Outputs	Output Frequencies	Fractional/ Integer	Phase Jitter 12kHz-20MHz	Footprint # of pins
Full System Synchronization										
Programmable bandwidth .25mH to 10 Hz, 1PPS support	NS2000	(PDF)	3	1PPS, 1Hz-125MHz	2	23	1PPS, 1Hz-800MHz	Integer	100fs, 250 fs	10x10mm QFN88
Master clock, NPLL, NCO, hitless switching, phase align	NS2004	(PDF)	3	1PPS, 1Hz-125MHz	2	18	1PPS, 1Hz-800MHz	Integer	100fs, 250 fs	8x8mm QFN68
Full Stratum and Sync E compliance, 2 timing generators,	STC5428	(PDF)	12	2KHz- 77.76MHz	0	8	2KHz -156.25MHz	Fractional	10 ps	8x8mm QFN68
master slave, autonomous switching, programmable	STC5420	(PDF)	12	2KHz- 77.76MHz	0	8	2KHz -156.25MHz	Fractional	10 ps	TQFP 100
phase align	STC5423	(PDF)	2	2KHz- 77.76MHz	0	2	2KHz -156.25MHz	Fractional	10 ps	TQFP 100
	STC5230	(PDF)	12	2KHz- 77.76MHz	0	8	2KHz -77.76MHz	Fractional	10 ps	TQFP 100
Complete Stratum 3E timing system	STC3800	(PDF)	4	2KHz- 77.76MHz	0	4	2KHz -77.76MHz	Fractional	10 ps	BGA144
Line Card Sync/Jitter attenuator/Frequency Translator										
Programmable bandwidth 10 Hz to 50 Hz	NL3106	(PDF)	2	8KHz-800 MHz	1	12	1.5KHz-800 MHz	Fractional	200 fs	7x7mm QFN48
Frequency Translator										
Jitter attenuating , selectable bandwidth	NF2004	(PDF)	1	8KHz-125 MHz	2	17	1.5KHz - 800 Mhz	Integer	100fs, 250 fs	8x8mm QFN68
	NF1011	(PDF)	1	8KHz-125 MHz	1	1	10 MHz - 160 MHz	Integer	100fs, 250 fs	4x4 mm QFN32
Clock Generator/Signal translator										
Crystal or clock signal input, differential/ single ended	NC2004	(PDF)	2	10 MHz-160 MHz	2	17	1.5 KHz - 800 MHz	Integer	100fs, 250 fs	8x8mm QFN68

CONNOR WINFIELD

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